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IEEE CNF IEEE Conference Proceeding

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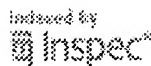
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Dept. of Electr. & Comput. Eng., Inst. of Commun. & Comput. Syst., Athens, Greece;

This paper appears in: [Computers and Communications, 1998. ISCC '98. Proceeding Symposium on](#)

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## Abstract

In this paper the design and the implementation of an ATM traffic generator system is presented. The principles of this document follow the architecture presented in Hontas et al. (1997). It is designed with a careful allocation of functions between hardware and software, in a way that it can operate at a high speed (155.52 Mbps) and, on the other hand, be flexible enough to emulate a wide range of traffic profiles (VBR and CBR). The system is composed of a basic traffic generator and ATM and four peripheral cards for the support of different physical interfaces. The architecture is open to any future modifications concerning signalling and functionality. The software of the system runs under Windows NT.

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## Author Keywords

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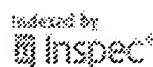
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## ATM burst traffic generator

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This paper appears in: **VLSI, 1995. Proceedings., Fifth Great Lakes Symposium on**

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## Abstract

The Markov modulated process is widely used to model ATM (Asynchronous Transfer Mode) traffic sources because it can emulate the bursty and correlated traffic arrival. We propose a high-speed generator emulating the bursty behavior of the Markov process and develop circuits for a Markov modulated process and an N-state homogeneous Markov modulated process. The generator traffic imitating cell arrival of various voice and video sources. Our test circuit can be fitted on a small sized FPGA chip and can generate up to 20M cell arrivals per second.

## Index Terms

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[Markov processes](#) [asynchronous transfer mode](#) [field programmable gate array](#) [telecommunication traffic](#) [visual communication](#) [voice communication](#)

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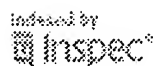
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